

Grams: "TECHNOLOGY"
Email: dejntukakinada@gmail.com



Off: 0884-2300911
Mobile : +91 7093925555

Directorate of Evaluation
JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA
KAKINADA-533003, Andhra Pradesh, INDIA

Dr. P. Subba Rao,
B.Tech., M.E., Ph.D.,
Professor of Civil Engineering &
Director of Evaluation

Lr. No. DE / Ph.D / 237 /EEE/ D. Srinivas (09022P0216) / 2016, Dt: 26.10.2016.

NOTIFICATION

Sub: Award of Ph. D Degree in faculty of Electrical & Electronics Engineering to Mr. D. Srinivas -
Reg.

The Thesis entitled "A Novel Method for Balancing of DC Link Capacitor Voltages of Multilevel Inverters" submitted by Mr. D. Srinivas, Roll No. 09022P0216 in Department of Electrical & Electronics Engineering has been accepted by the University. Based on the recommendations of the Panel of Examiners; Mr. D. Srinivas is provisionally declared qualified for the award of degree of Doctor of Philosophy in faculty of Electrical & Electronics Engineering.

Date: 26-10-2016.

// By Order//

Director of Evaluation

Copy to

1. Mr. D. Srinivas, Research Scholar – through the Director, R&D Cell, JNTUK, Kakinada.
2. Dr. B. Sarvesh, External Examiner Department of EEE, JNTU College of Engineering, JNTUA, Anathapur -515 002.
3. Dr. Dr. B.K Sankar Ram, Professor of EEE, University College of Engineering, JNTUH, Kukatpally, Hyderabad.
4. The Secretary to the Hon'ble Vice Chancellor JNTUK, Kakinada.
5. The Rector, JNTUK, Kakinada.
6. The Registrar, JNTUK, Kakinada.
7. The Director, R&D Cell, JNTUK, Kakinada.
8. The Director, Academic & Planning, JNTUK, Kakinada.
9. The Controller of Examinations, JNTUK, Kakinada.
10. The Director, Association of Indian Universities, Rouse Avenue, New Delhi.
11. The Public Relations Officer, JNTUK Kakinada.

PRINCIPAL
MAHAVEER
INSTITUTE OF SCIENCE & TECHNOLOGY
Bandlaguda, Hyd-500 005.

Department of Electrical and Electronics Engineering

University College of Engineering, Kakinada
(Autonomous)

J.N.T. University Kakinada

Kakinada – 533003, A.P, India



Certificate

This is to certify that the thesis entitled "A NOVEL METHOD FOR BALANCING OF DC LINK CAPACITOR VOLTAGES OF MULTILEVEL INVERTERS" is being submitted by **Mr.D Srinivas** (R.No: 09022P0216) in partial fulfillment for the award of **Doctor of Philosophy in Electrical and Electronics Engineering** to the Jawaharlal Nehru Technological University Kakinada is a record of bonafide work carried out by him under our guidance and supervision.

The results embodied in this thesis have not been submitted to any other University or Institute for the award of any degree or diploma.

Signature of the Supervisor

Dr.B.V.Sanker Ram,
Professor, Department of EEE
JNTU,Hyderabad

Dr. B.V. SANKER RAM
M.Tech
PRINCIPAL
Dept. of Electrical Engineering
JNTU COLLEGE OF ENGINEERING
HYDERABAD
MAHAVEER
INSTITUTE OF SCIENCE & TECHNOLOGY
Bendlaguda, Hyd-500 003.



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR
ANANTHAPURAMU – 515 002 (A.P.) – INDIA.**

Prof. S. V. SATYANARAYANA
Director of Evaluation

Lr. No.674/Ph.D/EE-23/N.V/EE/2018 dt. 07.06.2019

NOTIFICATION

Sub:- Award of Ph.D-Degree in Electrical Engineering to Mr. N.Rajasekhar Varma

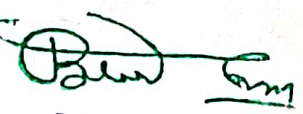
The thesis entitled "WAVELET TRANSFORM BASED HIGH IMPEDANCE FAULT DETECTION IN DISTRIBUTION SYSTEM USING ARTIFICIAL INTELLIGENCE AND KNN TECHNIQUES" submitted by Mr. N.Rajasekhar Varma, [H.T.No.10Ph0220] has been accepted by the University on the recommendation of the panel of examiners. Mr.N.Rajasekhar Varma is provisionally declared and qualified for the award of Doctor of Philosophy, in the faculty of Electrical Engineering. The viva voce examination is conducted on 31.05.2019.

Date: 07.06.2019

//By Order//


Director of Evaluation

1. Mr. N.Rajasekhar Varma – through the Director, R&D, JNTUA, Ananthapuramu.
2. Dr. B.V. Ssnker Ram, Professor, Dept of Electrical & Electronics Engineering, JNTU College of Engineering, Kukatpally, Hyderabad-500085. [Supervisor]
3. Dr.Yogendra Kumar, Professor, Dept EE, Moulana National Institute of Technology, Bhopal-462003. [External Examiner]
4. PA to Vice-Chancellor, Administrative Building, JNTUA, Ananthapuramu.
5. PA to Rector, Administrative Building, JNTUA, Ananthapuramu
6. PA to Registrar, Administrative Building, JNTUA, Ananthapuramu.
7. The Director, R&D, Administrative Building, JNTUA, Ananthapuramu.
8. The Director, Academic and Planning, Administrative Building, JNTUA, Ananthapuramu.
9. The Controller of Examinations (PG), Examination Branch, JNTUA, Ananthapuramu.
10. The Controller of Examinations (UG), Examination Branch, JNTUA, Ananthapuramu.
11. The Director Association of Indian Universities, Rouse Avenue, New Delhi.
12. The Public Relations Officer, Administrative Building, JNTUA, Ananthapuramu


PRINCIPAL
MADHAWEEER
INSTITUTE OF SCIENCE & TECHNOLOGY
Bandlaguda, Hyd-500 005.



E-Mail ID: drnd.vivavoce@jntua.ac.in
Phone & Fax: 08554-273006
Mobile: 9000 551420

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR
ANANTHAPURAMU-515 002(A.P) INDIA
Research & Development

Dr. A. Ananda Rao
Professor of CSE &
Director

CONFIDENTIAL

To

Lr.Rc.No.603-18/Ph.D.823/NRV/EEE/2018, Dt :28.03.2019

1. **Dr. B.V. Sanker Ram,**

Professor,
Dept. of Electrical & Electronics Engineering,
JNTU College of Engineering, Kukatpally,
Hyderabad-500085
Cell: 8500961061
Email Id: bvsram4321@yahoo.com

[Internal Examiner]

2. **Dr. Yogendra Kumar,**

Professor, Dept. of EE,
Moulana Natabional Institute of Technology,
Bhopal-462003
Cell: 08959662996
Email Id: ykmact@yahoo.co.in

[External Examiner]

Sir,

Sub: JNTUA – R & D – Ph.D. (EE) – Appointment of
Examiners – Request for date of Viva-Voce examination – Reg.

Ref: D.O.Lr.No.660/ Ph.D./EEE-23/NRVM/EE /2018, dt: 27.03.2019.

I am happy to inform you that you have been appointed as examiner for the conduct of Ph.D. Viva-Voce examination in respect of **Mr. N. Rajasekhar Varma** in the faculty of **Electrical & Electronics Engineering**.

Therefore, I request you to communicate any two dates convenient to you within two weeks after consultation with the second examiner so as to arrange for the conduct of Viva-Voce examination. Further, you are requested to provide the dates such that there should be a minimum of 10 days duration from the date of this orders. You will be paid TA/DA and Honorarium as per the University norms.


Yours faithfully

PRINCIPAL
MAHAVEER
DIRECTOR
INSTITUTE OF SCIENCE & TECHNOLOGY
Bandlaguda, Hyd-500 005.

Encl: Xerox Copies of thesis evaluation reports.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
(Established by JNTU Act No. 10 of 2008)
Kukatpally, Hyderabad - 500 085, Telangana (India)

Dr. V KAMAKSHI PRASAD
M.Tech., Ph.D. (IIT - Madras), IIT, MCSI
Professor of Computer Science and Engineering &
DIRECTOR OF EVALUATION

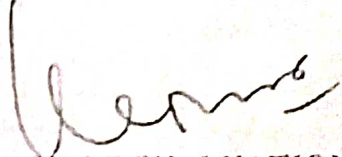
Ref: P7/Ph.D./1992/01-02/DRK/EEE/2019
Dt: 12.06.2020

NOTIFICATION


Sub : Award of Ph.D. Degree in Electrical & Electronics Engineering to Mr. Durgam Kumaraswamy (IT. No. 1203PH0783).

The thesis entitled "MOSFET Based DC-DC Converter Using Soft Switching Techniques for Improving Performance" submitted by Mr. Durgam Kumaraswamy has been accepted by the University on the recommendations of the panel of examiners. Mr. Durgam Kumaraswamy is provisionally declared and qualified for the award of Doctor of Philosophy in the faculty of Electrical & Electronics Engineering.

//By order//


DIRECTOR OF EVALUATION

1. Mr. Durgam Kumaraswamy - through the Director, DRD, JNTUH, Kukatpally, Hyderabad.
2. Dr. B V Sanker Ram, Retd. Professor, H.No. 1-42/PE/111, Plot No. 111, Pragathi Enclave Colony Miyapur, Hyderabad - 500049, Telangana State. [Supervisor]
3. Dr. D. M. Vinod Kumar, Professor, Department of Electrical Engineering, National Institute of Technology Warangal, Warangal - 506 004, Telangana State. [External Examiner]
4. PA to Vice-Chancellor, Administrative Building, JNTUH, Kukatpally, Hyderabad.
5. PA to Registrar, Administrative Building, JNTUH, Kukatpally, Hyderabad.
6. The Director, DRD, Administrative Building, JNTUH, Kukatpally, Hyderabad.
7. The Director, Academic and Planning, JNTUH, Kukatpally, Hyderabad.
8. The Controller of Examinations, JNTUH, Kukatpally, Hyderabad.
9. The Director, Association of Indian Universities, Rouse Avenue, New Delhi.
10. The Public Relations Officer, Administrative Building, JNTUH, Kukatpally, Hyderabad.

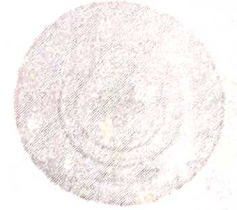

M. HANVEER
INSTITUTE OF SCIENCE & TECHNOLOGY
Bandlaguda, Hyd-500 095.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
HYDERABAD - 500 085, TELANGANA STATE, INDIA.



320001517

Sl. No. PC 0095685



PROVISIONAL CERTIFICATE



Hall Ticket No.: 1203PH0783

This is to certify that Mr. DURGAM KUMARASWAMY

son of Mr. PAPAIAH

has satisfied all the requirements for the award of Doctor of Philosophy

in the faculty of ELECTRICAL AND ELECTRONICS ENGINEERING

of this University, held in May, 2020 for the thesis entitled,

"MOSFET Based DC-DC Converter Using Soft Switching Techniques for Improving Performance".

[Signature]
Verified by

Hyderabad - T.S.

Date: 12 June 2020

[Signature]
Director of Evaluation

[Signature]
REGISTRAR

PRINCIPAL
MAHAVEER
INSTITUTE OF SCIENCE & TECHNOLOGY
Raidurg, Hyd-500 085.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
(Established by JNTU Act No. 30 of 2008)
Kukatpally, Hyderabad - 500 085, Telangana (India)

Dr. V KAMAKSHI PRASAD
M.Tech., Ph.D. (IIT - Madras), F.I.E., MCSI
Professor of Computer Science and Engineering &
DIRECTOR OF EVALUATION

Ref:- P7/Ph.D./19256/10-01/CT/EEE/2019
Dt : - 27.06.2020

NOTIFICATION

Sub: Award of Ph.D. Degree in Electrical & Electronics Engineering to Mr. Charansingh T,
(IIT. No. 1103PH0734).

* * *

The thesis entitled "Transient Stability Analysis and Stability Improvement of Multi Machine System with 6 Phase Transmission Systems" submitted by Mr. Charansingh T has been accepted by the University on the recommendations of the panel of examiners. Mr. Charansingh T is provisionally declared and qualified for the award of Doctor of Philosophy in the faculty of Electrical & Electronics Engineering.

//By order//

DIRECTOR OF EVALUATION

1. Mr. Charansingh T - through the Director, DRD, JNTUH, Kukatpally, Hyderabad.
2. Dr. B V Sanker Ram, Retd. Professor form JNTUH, H.No. 1-42/PE/111, Plot No. 111, Pragathi Enclave Colony, Miyapur, Hyderabad - 500 049, Telangana State. [Supervisor]
3. Dr. K Raghu Ram, Principal, Laqshya Inst. of Tech. & Science, Tanikella, Khammam - 507 305, Telangana State. [Co-Supervisor]
4. Dr. K. Shanti Swarup, Professor, Department of Electrical Engineering, Indian Institute of Technology Madras, Chennai - 600 036, Tamilnadu. [External Examiner]
5. PA to Vice-Chancellor, Administrative Building, JNTUH, Kukatpally, Hyderabad.
6. PA to Registrar, Administrative Building, JNTUH, Kukatpally, Hyderabad.
7. The Director, DRD, Administrative Building, JNTUH, Kukatpally, Hyderabad.
8. The Director, Academic and Planning, JNTUH, Kukatpally, Hyderabad.
9. The Controller of Examinations, JNTUH, Kukatpally, Hyderabad
10. The Director, Association of Indian Universities, Rouse Avenue, New Delhi.
11. The Public Relations Officer, Administrative Building, JNTUH, Kukatpally, Hyderabad.

PRINCIPAL
MAHAVEER
INSTITUTE OF SCIENCE & TECHNOLOGY
Bandlaguda, Hyd-500 005.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
HYDERABAD - 500 085, TELANGANA STATE, INDIA

TS 1437999



H.T No. : 1103PH0734

Mr. Charansingh T

Sh. Sundarsingh

having fulfilled the academic requirements in *June - 2020*

has this day been admitted by the Executive Council to the Degree of

Doctor of Philosophy

Electrical And Electronics Engineering

Topic: "Transient Stability Analysis and Stability Improvement of Multi Machine System with 6 Phase Transmission Systems".

Given under the Seal of the University

Date: 16-10-2020



Hepram

HEAD OF EVALUATION

[Signature]



49214116933

[Signature]

VICE CHANCELLOR

[Signature]
PRINCIPAL
MAHAVEER
INSTITUTE OF SCIENCE & TECHNOLOGY
Bandlaguda, Hyd-500 005.

PC No
320001540

ADMISSION LETTER

DR. A VINAYA BABU
DIRECTOR, ADMISSIONS

Lr No DA/H3/Ph.D/M.Phil/MS/Adms (EX)/2011
Dt. 12.09.2011

SUB : JNTUH - Admissions to External Research Programmes - 2011

1. You are provisionally selected for admission into Ph.D. programme in ELECTRICAL ENGG faculty subject to availability of vacancy at the guide and/or co-guide.
2. You are requested to report to the Director, Research & Development Cell, JNTUH, Kukatpally, Hyd-85 on or before 01.10.2011 and submit the following:
 - (i) I.C. Degree Certificate in Original (Provisional Certificate is not admissible)
 - (ii) Transfer Certificate in original
 - (iii) Fees:
 - A Demand Draft drawn on any nationalized bank for Rs. 20,700/- (Rs. 20,000/- one year fee + Rs 500/- caution deposit + Rs. 200/- admission fee) in favour of The Registrar, JNTUH, Hyderabad payable at Hyderabad.
 - Regular University employees have to pay of Rs. 200/- towards the admission fee.
 - (iv) Joining Report of the candidate, countersigned by the Research Supervisor(s) indicating the place where the Research work will be carried out.
 - (v) No Objection Certificate from the Head of the Organisation.
 - (vi) Latest Four passport size photographs.

3. Details of the Candidates:

(i) Name & Address:

T CHARAN SINGH
H NO 4-1-215
ROTARY NAGAR
KHAMMAM
507002

Supervisor(s) Name
Designation and Address
DR B V SANKER RAM
VICE-PRINCIPAL
JNTUH COLLEGE OF ENGG
KUKATPALLY
HYDERABAD-85

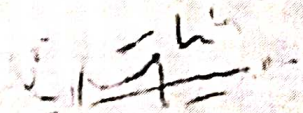
Co_Supervisor(s) Name
Designation and Address
DR K RAGHU RAM
PRINCIPAL
LAQSHYA INST OF TECH & SCIENCES
TANIKELLA-507305
KONIJERLA MDL, KHAMMAM

Topic of Research:

ANALYSIS OF TRANSIENT STABILITY WITH SMART GRID AND 6 PHASE TRANSMISSION SYSTEMS

4. From second year onwards for each year you will have to pay an amount of Rs 20,000/- (Rupees twenty thousands only) towards yearly fee before July 31 and obtain receipt.

To
200121
T CHARAN SINGH


DIRECTOR, ADMISSIONS

- Copy to:
- (1) The Director, R&D Cell, JNT University, Kukatpally, Hyderabad-85
 - (2) Supervisor are requested to check the list of candidates (given in JNTUH website) admitted under them till last year and then accept newly admitted candidate only if the no. of candidates under them are less than eight. Submit your list of eight candidates along with acceptance letter to Director R&D Section, JNTUH. After verifying the no. of candidates, the admission of new candidate will be approved.
 - (3) Co-Supervisor with a request to accept only if the no. of candidates are less than eight.

Note: The candidate should collect copy of guidelines for admission from the Director, R&D Cell, JNT University, Hyderabad.

MAHAVEER
INSTITUTE OF SCIENCE & TECHNOLOGY

Panduranga, Hyd-500 005

ANNAMALAI UNIVERSITY

(To be filled by Co-guide (applicable for External / Interdisciplinary))

Annexure-2

Signature of the Head of Institution
PRINCIPAL
 Christ Institute of Technology (CIT)
 Ramanathapuram Revenue Village,
 Thondamanatham Post,
 Puducherry-605 502.

Signature of the Co-Guide
PRINCIPAL guide
 Christ Institute of Technology (CIT)
 Ramanathapuram Revenue Village,
 Thondamanatham Post,
 Puducherry-605 502.

1	Name of the Applicant	J. VINOTHKUMAR		
2	Name of the Co-Guide	Dr. R. NAKKEERAN		
3	Date of Birth & Age	22.06.1965, 51		
4	Designation, Place of Working	PRINCIPAL, PUDUCHERRY		
5	Contact Address with Phone/Mobile e-mail	CHRIST INSTITUTE OF TECHNOLOGY, RAMANATHAPURAM VILLIYANUR, PUDUCHERRY. 9787718933, sudhand @gn		
6	Qualifications	M.E., Ph.D.,		
	Name of Degree	Specialisation (major)	College and University	Month & Year of Passing Class
	M.E	Computer Science	Jayaram College Engineering Anna University	May 2006 First with Distinction
	Ph.D	-di-	Anna University	May 2007
7	Title of dissertation for Ph.D Degree Programme	Hybrid intelligent computing approach for Data mining in Cancer Gene feature selection and Classification problem.		
8	No. of research papers published in accredited / indexed journals	9		
9	No. of books published / invited chapters contributed (enclose list)	1		
10	Total research experience (enclose details)	Years:	26	Months:
11	Positions held.	Professor & Principal.		
	Name of Institution	from	To	
	Christ Institute of Technology	13.8.2015	till date	
12	Subject / Discipline in which you propose to guide for Ph.D	Computer & Networks PRINCIPAL		
13	Whether already recognized as Ph.D. guide by an Institute of Science & Technology (enclose details).	MAHAVEER INSTITUTE OF SCIENCE & TECHNOLOGY Bandlaguda, Hyd-500 005.		
14	Total No. of candidates registered at present Annamalai University	3		

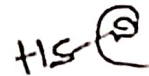
DECLARATION BY THE CANDIDATE

I declare that the thesis entitled "A STUDY ON DYNAMIC RESPONSE IN HYDROLIC EQUIPMENT WORKING WITH HEAVY LOADS" is my own work conducted under the supervision of DR. NILESH DIWAKAR at Sri Satya Sai University of Technology & Medical Sciences, Shohre, MP and Co-Supervisor of DR. B NAGESHWAR RAO, Mahaveer Institute of Science & Technology, Hyderabad approved by Research Degree Committee. I have put in more than 240 days of attendance with supervisor at the center.

I further declare that to the best of my knowledge, the thesis does not contain any part of any work which has been submitted for the award of any degree either in this University or in any other University without proper citation.

Place :Shohre(MP)

Date:



Signature of the Candidate



PRINCIPAL
MAHAVEER
INSTITUTE OF SCIENCE & TECHNOLOGY
Bandlaguda, Hyd-500 005.

DECLARATION BY THE CANDIDATE

I declare that the thesis entitled "OPTIMIZATION OF SPRAYING PROCESS IN THERMAL POWER PLANT USING ROBOT" is my own work conducted under the supervision of **DR. NILESH DIWAKAR** at Sri Satya Sai University of Technology & Medical Sciences, Sehore, MP and Co-Supervisor of **DR. B NAGESHWAR RAO**, Mahaveer Institute of Science & Technology, Hyderabad approved by Research Degree Committee. I have put in more than 240 days of attendance with supervisor at the center.

I further declare that to the best of my knowledge, the thesis does not contain any part of any work which has been submitted for the award of any degree either in this University or in any other University without proper citation.

Place :Sehore(MP)

Date:



Signature of the Candidate



PRINCIPAL
MAHAVEER
INSTITUTE OF SCIENCE & TECHNOLOGY
Bandlaguda, Hyd-500 005.

*Study on Ultrasonic propagation in Polymers and Molecular
Interaction Studies of Binary System using Ultrasonic
Interferometer Technique*

*Thesis submitted to Dravidian University in partial fulfillment
of the requirements for the degree of Doctor of Philosophy in Physics*

By

M. BALARAJU
(REG No. 01509223005)

Research Supervisor


Dr. K. MADHUSUDHANA

Assistant Professor,
Mahaveer Institute of Science and Technology
Bandlaguda, Hyderabad – 500 005.



DRAVIDIAN UNIVERSITY
KUPPAM, ANDHRA PRADESH

(2017)


PRINCIPAL
MAHAVEER
INSTITUTE OF SCIENCE & TECHNOLOGY
Bandlaguda, Hyd-500 005

UNDER CERTIFICATE OF POSTING
JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY
KUKATPALLY : HYDERABAD 500 072

DR. M CHANDRA SHEKAR
Coordinator, Admissions

Lr.No.CA/HS/HS/Ph.D/W,Ph.D/admins(PT)/2006
DL.00.00.2006

SUB : JNT University - Admissions To Part Time Research Programmes - 2006

1. You are provisionally selected for admission into Ph.D. programme in ELECTRICAL ENGINEERING faculty
2. You are requested to report to the Director, Research & Development Cell JNT University, Kukatpally, Hyderabad-72 on or before 00.09.2006 and submit the following:
 - (i) P.G.Degree Certificate in Original (Provisional Certificate is not admissible)
 - (ii) Transfer Certificate in Original
 - (iii) Fees:
A Demand Draft drawn on any nationalized bank for Rs.20,700/- (Rs.20,000/- one year fee + Rs.500/- caution deposit + Rs.200/- admission fee) in favour of The Registrar, JNTU, Hyderabad payable at Hyderabad.
Regular University employees have to pay of Rs.200/- only towards the admission
 - (iv) Joining Report of the candidate, countersigned by the Research Supervisor(s), indicating the place where the Research work will be carried out.
 - (v) No Objection Certificate from the Head of the Organisation.

3. Details of the Candidates:

(i) Name & Address:

D OBULESU
TEACHER
ZPIIS, KAMALAPURAM
KADAPA DIST
516289

(ii) Faculty in which admitted into Ph.D. programme : ELECTRICAL ENGINEERING

(iii) Centre for Pre-Ph.D. Exam:

THE DIRECTOR, RESEARCH & DEVELOPMENT CELL, JNT UNIVERSITY, KUKATPALLY, HYDERABAD-72

Supervisor(s) Name
Designation and Address

Co_Supervisor(s) Name
Designation and Address

DR S F KODAD
PROFESSOR & HOD
DEPT OF EEE
MURORAS ENGG COLLEGE
BHONGIR-506116

DR B V SANKER RAM
PROFESSOR
DEPT OF ELECTRICAL ENGG
JNTU COLLEGE OF ENGG
KUKATPALLY, HYD-72

Topic of Research:

DESIGN AND IMPLEMENTATION OF ROBUST UNIFIED POWER FLOW CONTROLLER FOR DAMPING POWER SYSTEM OSCILLATION

4. From second year onwards for each year you will have to pay an amount of Rs.20,000/- (Rupees twenty thousand only) towards yearly fee before July 31 and obtain receipt.

To
D OBULESU

COORDINATOR - ADMISSIONS

Copy to: (1) The Director, R&D Cell, JNT University, Kukatpally, Hyderabad-72
(2) Supervisor
(3) Co-Supervisor

PRINCIPAL
MA VEEER
INSTITUTE OF SCIENCE & TECHNOLOGY
Bandlaguda, Hyd-500 085

Name of the candidate should be written in the space provided below.

Signature of the Director, R&D Cell, JNT University, Kukatpally, Hyderabad-72